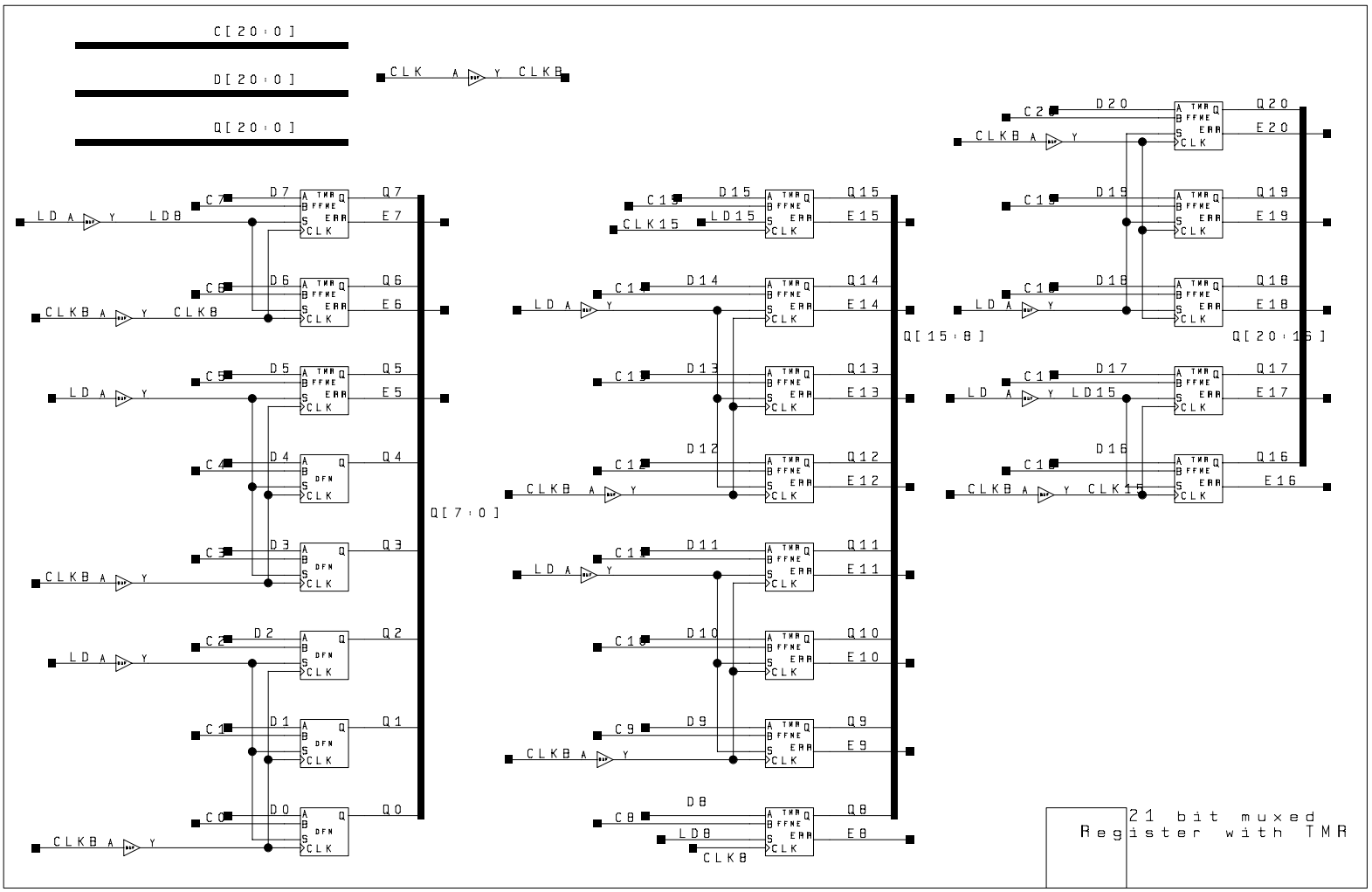
C[20:0]
D[20:0]
Q[20:0]
CLK
CLKB
LD
LD15
CLK15
Q[7:0]
Q[15:8]
Q[20:16]
TMR FFNE ERR
DFN
21 bit muxed
Register with TMR