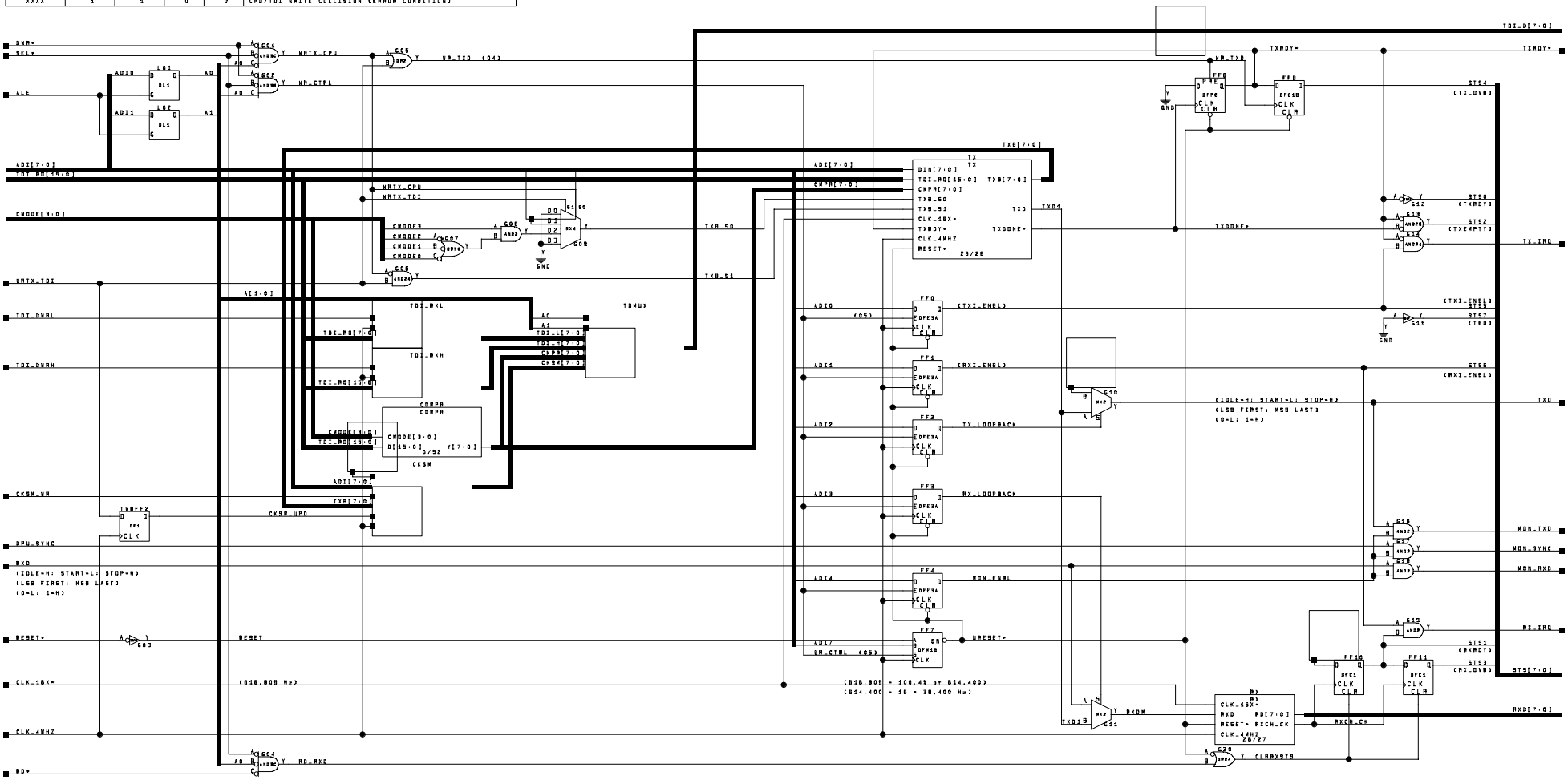
UART TX BUFFER WRITE FUNCTIONS					
CMODE[3:0]	WRTX_TDI	WRTX_CPU	TXB_91	TXB_90	FUNCTION
XXXX	0	0	0	0	IDLE
XXXX	0	1	0	1	CPU WRITE TO TX
0XXX	1	0	1	0	TDI WRITE TO TX, 1ST OR 2ND BYTE OF UNCOMPRESSED WORD
1000-1110	1	0	1	1	TDI WRITE TO TX, COMPRESSED BYTE
1111	1	0	1	0	TDI WRITE TO TX, HIGH 8 BITS OF 12-BIT TDI WORD
XXXX	1	1	0	0	CPU/TDI WRITE COLLISION (ERROR CONDITION)



C.A.Ingraham 1997-11-04

C.A.Ingraham 1998-09-24

Schematic.
DPUGLU Actel UART Block

APPROX. SEQ/COMB MODULE COUNT: 87/143

\IMAGE\...\DPUGLU\UART