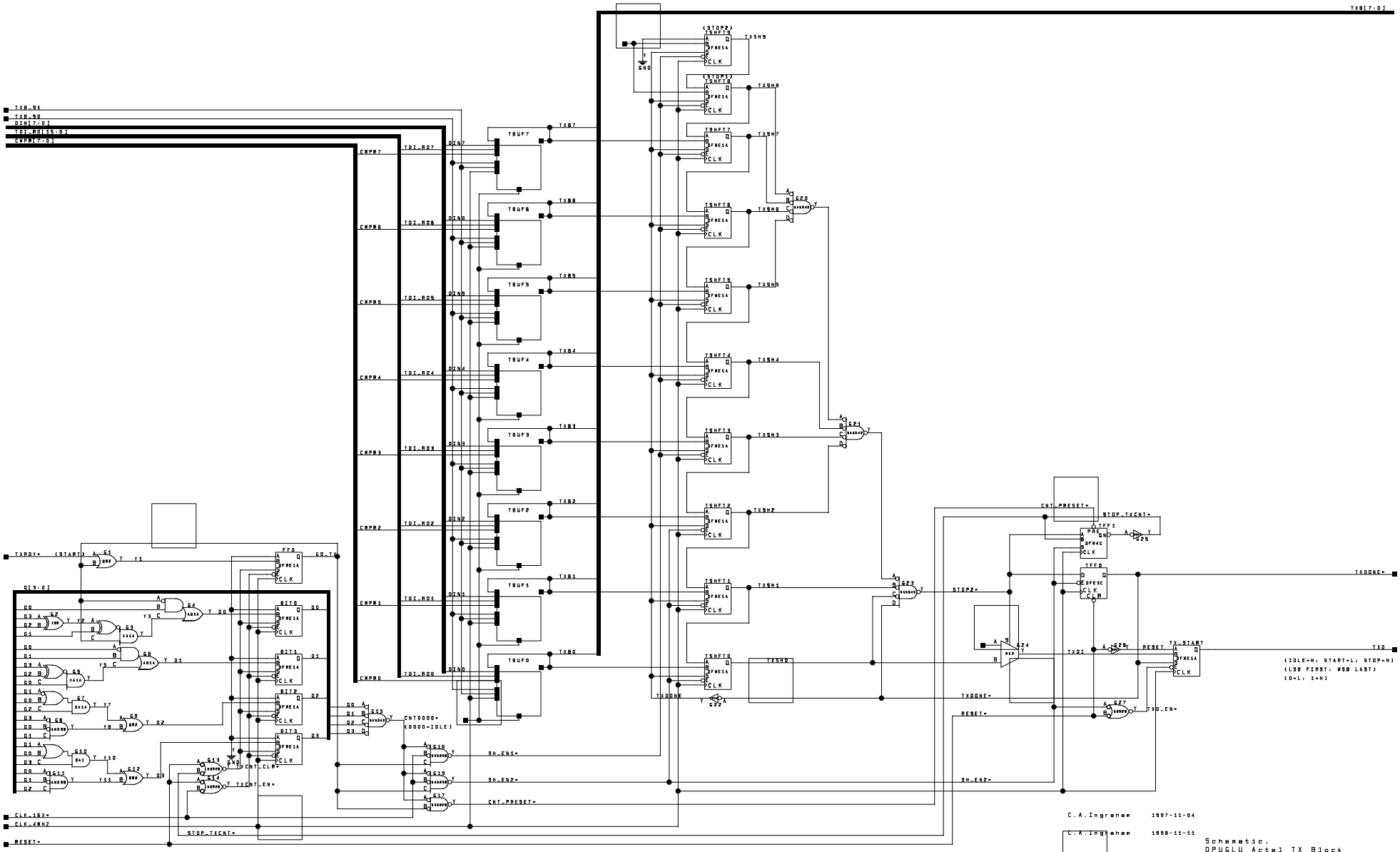
TX8[7:0]
TXR_91
TXR_90
DIN[7:0]
TDI_RD[15:0]
CMPR[7:0]
TXRDY+
(START)
Q[3:0]
CLK_16X+
CLK_4MHZ
RESET+
STOP_TXCNT+
TXCNT_CLK+
TXCNT_EN+
CNT0000+
(0000=IDLE)
SH_EN4+
SH_EN2+
CNT_PRESET+
TXDONE+
STOP2+
TXD1
TXD
TXD_EN+
TX_START
(IDLE=H; START=L; STOP=H)
(LSB FIRST; MSB LAST)
(0=L; 1=H)
C.A.Ingraham 1997-11-04
C.A.Ingraham 1998-11-11
Schematic,
DPUGLU Actel TX Block
APPROX. SEQ/COMB MODULE COUNT: 28/28
\IMAGE\...\DPUGLU\TX
1 of 1
A