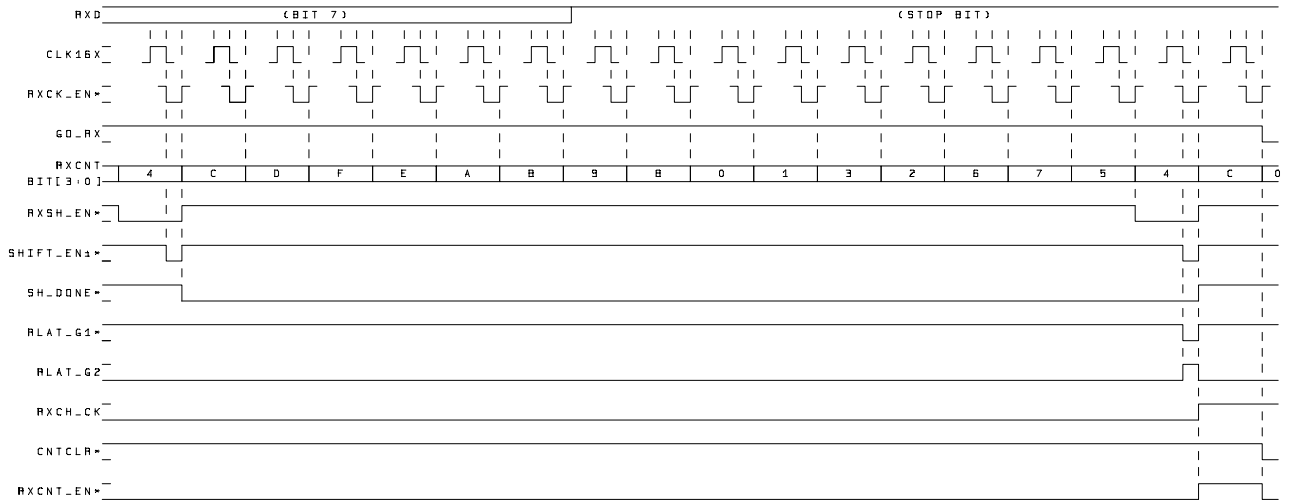


CTTL (REF)
(10.8 MHz)



C. A. Ingraham 22 FEB 1994

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Timing Diagram. IDPU Processor
PDUART Act1 Receive Stop Bit

\FAST\IDPU\PROC\PDUART\TD_RXSP 1 of 1 F