



PHA12	PHA13	FUNCTION
S0 RD	S2 RD	LOCK BUFFER & READ LQX (Y) BYTE
S1 RD	S3 RD	READ HIGH (X) BYTE, UNLOCK BUFFER, & CLEAR TEST MODE
S0 WR	S2 WR	SET TEST MODE

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Schematic, PHA Block
 DPUGLU Act1 PHA Block

APPROX. MODULE COUNT: 90/45

\\IMAGE1\...DPUGLU\PHA