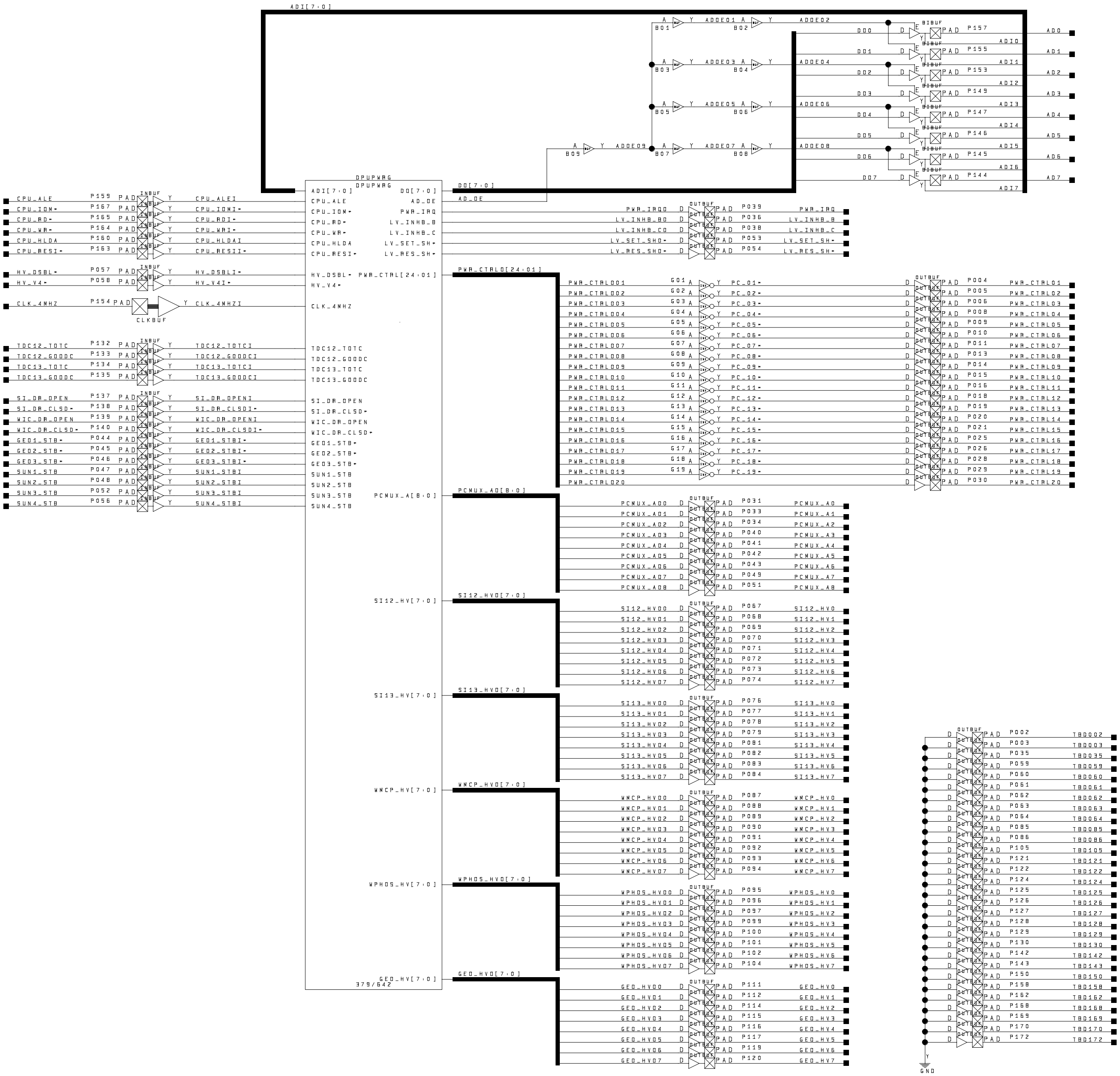

A	1998-01-17	FIRST RELEASE.
B	1998-04-08	Stretch SET/RES_SHUNT pulses to 16.7 ms minimum; make LV_RES_SH* active with reset; fix PCMUX_A7 decode error; fix ACNTR8B clocks; add rev port; add counter half-full status bits; invert PWR_CTRL[19:01] outputs; add high voltage write enable code; add inputs and logic for sun sensors 3 and 4; add raw sun sensor status port; use slower GEO test clock; rename door switch signals.
C	1998-06-04	Keep LV_RES_SH* high during reset; update rev port.
D	1998-09-18	Change Rev port to 04h; add MAX registers for WIC HV.
E	1998-10-13	Change Rev port to 05h; fix OMUX addr in REGHVM.
F	1998-10-30	Change Rev port to 06h; reset HVC reg when PS is off (REGHV Rev B); disallow disabling all four sun sensors; disable sun sensor test logic; undo HV reg reset (REGHV Rev C).
G	1998-12-23	Reset LV_SET_SH FF during reset
H	1999-01-04	Change Rev port to 08h; correct ACNTR8B CLK input sense.

IO ADDR	IOWR FUNCTION	IORD FUNCTION
80-93	PWR_CTRL[20:01] AS FOLLOWS:	
80	01: TDI12 +5V	DPUPWR REV NUMBER
81	02: TDI13 +5V	GEO/TDC COUNTER STATUS
82	03: WTDI +5V	DOOR SWITCH STATUS
83	04: GEO HV +/-12V	(TBD)
84	05: WIC HV +/-12V	(TBD)
85	06: SI12 HV +/-12V	(TBD)
86	07: SI13 HV +/-12V	(TBD)
87	08: WIC FEC +5V, +/-12V	(TBD)
88	09: WIC DOOR PRIME +5V	(TBD)
89	10: WIC DOOR BACKUP +5V	(TBD)
8A	11: SI DOOR PRIME +5V	(TBD)
8B	12: SI DOOR BACKUP +5V	(TBD)
8C	13: WIC HEATER +12V	(TBD)
8D	14: SI BAKE HTR +12V	(TBD)
8E	15: SI GRAT HTR -12V	(TBD)
8F	16: SI COLL HTR -12V	(TBD)
90	17: TDC12 +/-5V, SI12 PA +5V	PWR_CTRL[08:01] STATUS
91	18: TDC13 +/-5V, SI13 PA +5V	PWR_CTRL[16:09] STATUS
92	19: GEO PREAMP +5V	PWR_CTRL[24:17] STATUS
93	20: MUX/ADC +/-12V	LV STATUS
94	WMCP NORM HV MAX	WMCP NORM HV MAX
95	WMCP SUN HV MAX	WMCP SUN HV MAX
96	WPHOS NORM HV MAX	WPHOS NORM HV MAX
97	WPHOS SUN HV MAX	WPHOS SUN HV MAX
98	(TBD)	GEO1 COUNT LOW (1ST)
99	GEO1 TEST MODE	GEO1 COUNT HIGH (2ND)
9A	(TBD)	GEO2 COUNT LOW (1ST)
9B	GEO2 TEST MODE	GEO2 COUNT HIGH (2ND)
9C	SUN LOGIC TEST	GEO3 COUNT LOW (1SR)
9D	GEO3 TEST MODE	GEO3 COUNT HIGH (2ND)
9E	SUN LATCH CLEAR	SUN LATCH STATUS
9F	SUN SENSOR CONTROL	SUN SENSOR STATUS
A0	SI12 NORMAL HV SETTING	SI12 NORMAL HV SETTING
A1	SI12 SUN HV SETTING	SI12 SUN HV SETTING
A2	HV UNLOCK CODE 0	SI12 HV DAC INPUT
A3	SI13 NORMAL HV SETTING	SI13 NORMAL HV SETTING
A4	SI13 SUN HV SETTING	SI13 SUN HV SETTING
A5	HV UNLOCK CODE 1	SI13 HV DAC INPUT
A6	WIC MCP NORMAL HV SETTING	WIC MCP NORMAL HV SETTING
A7	WIC MCP SUN HV SETTING	WIC MCP SUN HV SETTING
A8	HV UNLOCK CODE 2	WIC MCP HV DAC INPUT
A9	WIC PHOS NORMAL HV SETTING	WIC PHOS NORMAL HV SETTING
AA	WIC PHOS SUN HV SETTING	WIC PHOS SUN HV SETTING
AB	HV UNLOCK CODE 3	WIC PHOS HV DAC INPUT
AC	GEO NORMAL HV SETTING	GEO NORMAL HV SETTING
AD	GEO SUN HV SETTING	GEO SUN HV SETTING
AE	(TBD)	GEO HV DAC INPUT
AF	(TBD)	HV STATUS
B0	SW HV V/4 CONTROL	TDC12 TOTAL EVENT COUNT LOW (1ST)
B1	SELECT PC MUX CHANNEL	TDC12 TOTAL EVENT COUNT MID (2ND)
B2	INHIBIT LVPS B	TDC12 TOTAL EVENT COUNT HI (3RD)
B3	INHIBIT LVPS C	TDC12 TOTAL EVENT COUNT TEST MODE
B4	RESET LV SHUNT	TDC12 GOOD EVENT COUNT LOW (1ST)
B5	SET LV SHUNT	TDC12 GOOD EVENT COUNT MID (2ND)
B6	(TBD)	TDC12 GOOD EVENT COUNT HI (3RD)
B7	(TBD)	TDC12 GOOD EVENT COUNT TEST MODE
B8	(TBD)	TDC13 TOTAL EVENT COUNT LOW (1ST)
B9	(TBD)	TDC13 TOTAL EVENT COUNT MID (2ND)
BA	(TBD)	TDC13 TOTAL EVENT COUNT HI (3RD)
BB	(TBD)	TDC13 TOTAL EVENT COUNT TEST MODE
BC	(TBD)	TDC13 GOOD EVENT COUNT LOW (1ST)
BD	(TBD)	TDC13 GOOD EVENT COUNT MID (2ND)
BE	(TBD)	TDC13 GOOD EVENT COUNT HI (3RD)
BF	(TBD)	TDC13 GOOD EVENT COUNT TEST MODE

APPROX. SEQ+COMB MODULE COUNT: 379 + 651 = 1030

C.A.Ingraham 1998-01-17
C.A.Ingraham 1999-01-04
C.A.Ingraham

Schematic,
MEP DPU DPUPWR Actel

8161-D3
\IMAGE\...\DPUPWR\DPUPWR

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