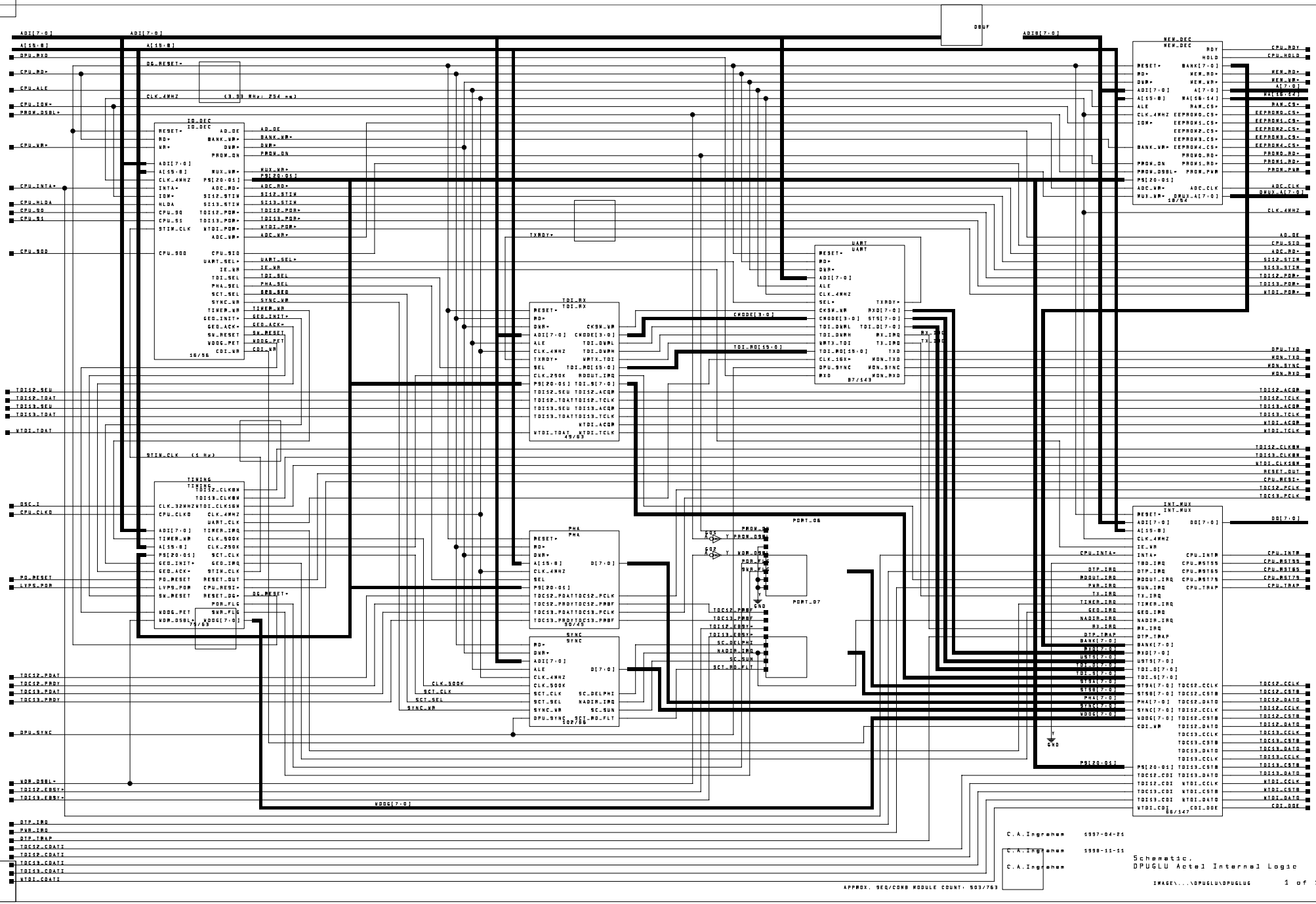
IO_DEC
TDI_RX
UART
MEM_DEC
TIMING
PHA
SYNC
INT_MUX
C.A.Ingraham 1997-04-21
C.A.Ingraham 1998-11-11
C.A.Ingraham
Schematic,
DPUGLU Actel Internal Logic
IMAGE\...\DPUGLU\DPUGLUG
1 of 1
A
APPROX. SEQ/COMB MODULE COUNT: 503/763