REV	DESCRIPTION	BY	DATE
A	GENERATED FROM AS-BUILT FLIGHT PCB	HDH	1AUG99

INTERFACE CABLE BETWEEN WIC CAMERA AND WIC TDI BOARD. SHOWN FOR REFERENCE ONLY. SEE DWG 8389-A4 FOR DEFINITIVE DRAWING OF THIS CABLE.

ARTN 1
+5VA 26
+5VA 2
+12VA 27
-5VA 3
-12VA 28
+27V 49

D7H 29
D7L 5
D6H 30
D6L 6
D5H 31
D5L 7
D4H 32
D4L 8
D3H 33
D3L 9
D2H 34
D2L 10
D1H 35
D1L 11
D0H 36
D0L 12

MONH 42
MONL 18

SHIELD-TERM 13
SHIELD-TERM 16

CCLK 41
CDAT 40
CSTR 39
AQR 17
ACCUM 43

PIXL 19
PIXH 44
FRML 21
FRMH 46

LINL 20
LINH 45

NOTE:
PINS 24 AND 27 ARE TIED TOGETHER ON THE ETU WIC TDI BOARD. STATUS OF FLIGHT BOARD NOT KNOWN.

MCLKL 47
MCLKH 23

+5VD 24

DRTN 22
DRTN 48
ARTN 25
ARTN 50

SPARE-4 37
SPARE-5 4
SPARE-2 14
SPARE-1 15

THERE IS NO CONNECTION TO PIN 38 IN 'BLUE' ETU CABLE

AIRBORN WTB50SAP20SY-K55
J2

+5V
D17 1N4148
100K R182
1K R159
C88 .01uF

U2
CCLK 41 → 36 B4
CDAT 40 → 38 A4
CSTB 39 → 40 B3
ACQ 17 → 34 A5
ACCUM 43 → 31 C5

R4

U3
PIXL 19 → 15 A6
PIXH 44 → 13 B6
FRML 21 → 5 C7
FRMH 46 → 3 A8

NOT ASSIGNED (WAS LINL?) 20 → 10 C6
NOT ASSIGNED (WAS LINH?) 45 → 8 A7

L2
C8
C9
3145728 Hz OSC
168 C11
GMCLK

+5V
L3
C5 C10 C11 C6 C7
VCC

GND

POR 42 A3

ON R1280 ACTEL:
ACTEL PINS 12, 23, 24, 27, 50, 66, 80, 107, 109, 110, 113, 136, 151, AND 166 ARE TIED TO +5V.
ACTEL PINS 7, 17, 22, 32, 37, 55, 65, 75, 98, 103, 106, 108, 118, 123, 141, 152, 161 ARE GROUNDED

ACTEL 1020B
OR
ACTEL R1280
U1

CLKIN F9
RMCLK A10
ANAL1 B8
ANAL2 A9
FORMER TEST POINTS NOW NOT USED. CONNECTED TO GROUNDED BUFFERS IN ACTEL.

PRIA D10
PRIB D11
(NOT USED) FORMERLY WAS COMPLEMENTARY OUTPUTS TO RUN +27V POWER CONVERTER

NOT ASSIGNED (WAS RMS3?)

AIRBORN WTB50SAP20SY-K55
J1

R1
RM3C L1 88 → 14 39 1 → M3C 26
RM1C L2 92 → 13 39 2 → M1C 2
RM2C K3 90 → 12 39 3 → M2C 27
RADCLK L3 94 → 11 39 4 → ADCLK 5
RF3C K4 96 → 10 39 5 → F3C 30
RF2C L4 97 → 9 39 6 → F2C 6
RF1C J5 99 → 8 39 7 → F1C 31

R2
RSUMC L6 111 → 14 39 1 → SUMC 12
RRGC J7 114 → 13 39 2 → RGC 13
RADCSA L7 116 → 12 39 3 → ADCSA 38
RADCSB K8 119 → 11 39 4 → ADCSB 14
RDATE L8 121 → 10 39 5 → DATE 15
RI3C K9 124 → 9 39 6 → I3C 19
RI2C L9 126 → 8 39 7 → I2C 44
RI1C L10 128 → 39 → I1C 20

R3
RVP3A0 L11 130 → 51 → VP3A0 10
RVP3A1 K10 132 → 14 51 1 → VP3A1 35
RVP12A0 K11 134 → 13 51 2 → VP12A0 11
RVP12A1 J10 135 → 12 51 3 → VP12A1 36
RMS0 J11 137 → 11 51 4 → MS0 40
RMS1 H10 138 → 10 51 5 → MS1 16
RMS2 H11 140 → 9 51 6 → MS2 17
? G9 → 8 51 7 → MS3 (?) 42
RMS3 F11 144 → 51 → MS3 47

DATE 15
ADCLK 5
ADCSA 38
OVERFLOW 8
ADCSB 14
VREF 25

M3C 26
M1C 2
M2C 27
HORIZONTAL SHIFT CLOCKS FOR CCD.

ADCLK 5
ADC CLOCK

F3C 30
F2C 6
F1C 31
VERTICAL SHIFT CLOCKS FOR MASKED PORTION OF CCD.

SUMC 12
CCD SUMMING GATE CLK

RGC 13
CCD HORIZ RESET

ADCSA 38
ADC A CHIP ENABLE

ADCSB 14
ADC B CHIP ENABLE

DATE 15
ENABLE ADC AC240

I3C 19
I2C 44
I1C 20
VERTICAL SHIFT CLOCKS FOR EXPOSED PORTION OF CCD.

VP3A0 10
VP3A1 35
VP12A0 11
VP12A1 36
VERTICAL SHIFT CLOCKS UPPER LEVEL ADJUSTMENT (NOT USED?)

MS0 40
MS1 16
MS2 17
MS3 42
MONITORS ANALOG MULTIPLEXOR DRIVE. (NOT USED?)

MS3 47

USED BY EARLY GSE ONLY

N.U. 13
N.U. 28

IMAGE FUV
UNIVERSITY OF CALIFORNIA AT BERKELEY
SPACE SCIENCES LABORATORY

TITLE: WIC CAMERA DIGITAL BOARD SCHEMATIC

REVIEWS (NAME/DATE) ORIGINAL VERSION	CURRENT REVISION	DRAWN BY	DATE	SIZE	DWG NO	REV
CHECK		H. HEETDERKS	7 AUGUST, 1999		8413-A4	A
RELEASE		ECO (NOTE)	SHEET 1 OF 1			